

nRF24Z1 Stamp Board Pin Assignments

3.3V	1	24	MCLK
IR1/DO[3]/PWM	2	23	I2S_CLK
SSEL/DO[2]	3	22	I2S_WS
SMISO/SSDA/DO[1]	4	21	I2S_DATA
SSCK/SSCL/DO[0]	5	20	SPDIO
SCSN/SADR/DI[3]	6	19	MSCK
SMOSI/DD[2]/DI[2]	7	18	MMOSI
DD[1]/DI[1]	8	17	MMISO
DD[0]/DI[0]	9	16	MCSN
REQ	10	15	MSCL
GND	11	14	MSDA
GND	12	13	MODE

Pin 1 is the pin with the large white dot over it (pins 12 and 13 are beside the chip antenna/RP-SMA connector). The above diagram is shown in top view. All adjacent pins are 0.1" apart, and the rows are 0.6" apart.